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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/937,562	10/15/2001	Shigeru Kitsutaka	110712	2254
7590 Oliff & Berridge PO Box 19928 Alexandria, VA 22320	04/17/2007		EXAMINER LEIVA, FRANK M	
			ART UNIT 3714	PAPER NUMBER
SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE		
3 MONTHS	04/17/2007	PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No.	Applicant(s)	
	09/937,562	KITSUTAKA, SHIGERU	
	Examiner	Art Unit	
	Frank M. Leiva	3714	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).

Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 02 September 2005.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-33 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1-33 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application
Paper No(s)/Mail Date _____	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

1. This Office Action is responsive to the communication filed 9/02/2005.

Amendments to the claims to traverse the 35 U.S.C. 101 rejections are noted and accepted.

The 35 U.S.C. 101 rejections to the claims are withdrawn.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. **Claims 1-33 are rejected under 35 U.S.C. 103(a) as being unpatentable over Baldwin (USPN 5,835,096) and further in view of Smith et al. (USPN 6,599,194 B1).**

In regards to:

4. Claims 1, 12, and 23, Baldwin discloses a system which generates an image comprising: means which transforms a depth value of each pixel of an original image into a second depth value formed of lower bits of a pixel which are positioned lower than the most significant bit of the depth value (see col. 7: ln 1-22, col. 19: ln 1-15, col. 30: ln 45-col. 31: ln 8, col. 33: ln 65-col. 34: ln 67, col. 50: ln 1-8); means which sets an alpha value of each pixel to a value corresponding to the second depth value; and means which generates an image based on the set alpha value (see col. 53: ln 50-col. 54: ln 10).

5. Claim 2, 13, and 24, Baldwin discloses a system wherein the original image is blended with a defocused image of the original image based on the alpha value set for each pixel. The process of alpha blending distorts the transparency of an original image in order to allow for the two objects to appear blended together (see col. 53: ln 65-col. 54: ln 45).

6. Claim 3, 14, and 25, Baldwin discloses a process wherein the defocused image of the original image is generated by disbursing the original image into a texture buffer and shifting the texture buffer coordinates of a virtual objects disbursed in a similar and then mapping the

two objects together through texel interpolation (see col. 53: *In 1-60*, col. 53: *In 65-col. 54: In 11*).

7. Claim 4, 15, and 26, Baldwin discloses a method of alpha blending wherein the second depth value is clamped into a give value depending on a bit value contained in the pixel information in the buffers (see col. 39: *In 64-col. 40: In 67*).

8. Claim 5, 16, and 27, Baldwin discloses the use of buffers and lookup tables to store the index numbers of depth values which are used for index color texture-mapping and the depth value is retrieved to be transformed into a second depth value by performing index color texture-mapping on a virtual object by using the lookup table (see col. 51: *In 60-col. 52: In 67*).

9. Claim 6, 17, and 28, Baldwin discloses a system of combining two images to maintain texture and realism uses bit information stored in the buffers to retrieve the depth values (i.e.: Z-buffers) that are set in the original image (see col. 3: *In 23-31*, col. 6: *In 65-col. 7: In 22*). These are then compared and interpolated to create a third and fourth depth value (i.e.: the transformed depth values of the original two images) and used to determine the new second depth value (see col. 19: *In 1-40*, col. 45: *In 1-45*, col. 48: *In 29-col. 50: In 33*, col. 51: *In 60-col. 52: In 40*).

10. In reference to claims 7 and 18, Baldwin discloses an image processing system, which generates an image comprising to be used to provide enhanced graphics capabilities. The processing system has been implemented to allow for animated images, which utilizes concepts such as textural interpolation and alpha blending (see col. 29: *In 10-24*, col. 29: *In 31-43*, col. 39: *In 40-60*, col. 53: *In 60-col. 54: In 11*). Alpha blending is a well-known and common implementation in the MMX instruction set under the x86 architecture that utilizes a portion of a pixel's stored data to control transparency. As a result it forms a mask effect where an image may be overlaid upon another so that typical objects may appear to blend into the background of screen giving the appearance that they appear as one image. This is a basic concept that allows for modern day 3-D rendering of images and graphics. The implementation of this method includes using several buffers, commonly known as lookup buffers, that contain the information of the two images containing the index color texture-mapping information so that the two may be observed and compared by the processor in

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order to properly blend the two images together (see col. 6: *In 65*-*col. 7: In 22*, col. 53: *In 30-64*). This is discussed in Baldwin where a means which sets bits in a given image information as an index number in a first lookup table for index color texture-mapping is set up (see col. 19: *In 1-15*, col. 23: *In 40-60*). At this time the first lookup table from a source image or object to transform the image information into a third a destination buffer (i.e.: third image). Additionally the same process is done on the 2nd image through a same image that will be overlaid on top of the previous image in a destination buffer (i.e.: fourth image) in order to form a new buffer that contained the third and fourth information to be displayed on the screen (see col. 29: *In 5*-*col. 31: In 5*). This information is transferred in the traditionally in the form of 32-bit words (24 bits for color information (RGB) and another 8 for the alpha level).

11. In reference to claims 8-11, 19-22, and 30-33, Baldwin discloses the creation of two overlapping images through the use of alpha blending as described above; the ability for a virtual object that is a polygon (essential for 3-D texturization in the computer graphics art) having a size equal to a size of a display screen (see FIGS. 1-3, col. 7: *In 8-22*, col. 25: *In 50*-*col. 26: In 5*); and a game system wherein the virtual object is a polygon having a size equal to a size of a block obtained by dividing a display screen into blocks. Display screens are inherently divided into blocks as screens are made up of standard pixels in order to create the object that is to appear on the screen.

12. In regards to claims 1, 7, 12, 18, and 23, Baldwin lacks in disclosing the use of his system with a game system. Although he does implement it on a computer with structural means which are analogous to the game system consoles.

13. In regards to claims 1, 12, and 23, Smith et al. discloses a method of implementing alpha blending using a game system, through the overlaying of a TV signal on a video game image (see col. 15: *In 10-20*, col. 16: *In 20-34*). It would therefore be obvious to one of ordinary skill in the art at the time of the invention to combine the teachings of Baldwin with Smith in order to implement the teachings of Baldwin into a system such as Smith's in order to reduce the amount of data to copy when a window is modified increasing speed.

14. In regards to claims 7 and 18, Smith et al. discloses the use of alpha blending to be implemented on a video game system. Smith teaches the use alpha in order to blend the video game with a picture-in-picture capability so that a TV signal maybe overlaid on the

display screen (see col. 15: ln 10-20, col. 16: ln 20-34). One would be motivated to take the teachings of Smith one step farther and implement the additional feature of alpha blending into a video game program in order to create a more realistic animated graphical experience for the user, but the benefits of alpha (transparency) values are well known in the art and necessary not only to create the overlays, but to texturize the 3-D picture in 2-D graphics generation. Therefore it would be obvious to one of ordinary skill in the art at the time of the invention to combine the teachings of Baldwin with Smith in order to have a game system that implemented the use of the alpha-blending concept.

Citation of Prior Art

15. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Oki et al. (US 6,559,854 B2)- Image creation device. Kenworthy et al. (US 5,808,617)- Method and System for Depth Complexity Reduction in a Graphics Rendering Systems. Narayanaswami (US 5,844,571)- Z Buffer Bandwidth Reductions via Split Transactions.

Response to Arguments

16. Applicant's arguments filed 09/02/2005 have been fully considered but they are not persuasive for the following reasons:

17. In regards to the argument directed to the rejections of claims 1, 12, and 23, "*Baldwin does not specifically teach or suggest transforming the depth value of each pixel of an original image into a second depth value formed lower bits which are positioned lower than the most significant bit of the depth value*", the examiner explains that *converting a value is the same as transforming, and that a right justification of the bits in the depth field so that the unused bits are the most significant, also means positioning the depth value to the lower bits reducing the value to a value lower than the first depth value*. As expressed by Baldwin, "The GID, FrameCount, Stencil and Depth fields in

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the local buffer are converted into the internal format by right justification if they are less than their internal widths, i.e. the unused bits are the most significant bits and they are set to 0", (col. 24: ln 63-67).

18. In regards to the argument directed to the rejections of claims 1, 12, and 23,"col. 1, lines 15-21, Baldwin is related to computer graphics and animation system, which is apparently connected to a display system. Smith merely teaches a game system with connectivity to a TV. Therefore, Applicants respectfully assert that such incorporation would results in a mere change in the way to output the image generated", the examiner understands that Baldwin's invention is directed to computer graphics and that it needs to be combined with the disclosure of Smith, whereas Smith discloses a game embodiment and means to display it graphically, Smith's embodiment also includes a computer console and means for creating the picture before sending it to the display, "*This enables a computer system embodied within console 52 to access the information contained within read only memory 76, which information controls the console computer system to play the appropriate video game by displaying images and reproducing sound on color television set 58 as specified under control of the read only memory game program information*", (col. 4: ln 49-55). Furthermore and after consideration, the Smith reference is not necessary due to the disclosed present invention, "*The present invention also provides a computer-usable information storage medium comprising a program for a computer to realize the above-described means. The present invention further provides a computer-usable program (including a program embodied in a carrier wave) comprising a processing routine for a computer I/O to realize the above-described means*", (Specs. pg 3: ln 4-10), where the invention is created for use in a computer and thus requires computer graphics generation and display.

In regards to the arguments directed to the rejections of claims 7 and 18, "*recites, inter alia, means which determines second image information formed of bits I to J (where K > I*

> L > M > J > N) in the image information based on third and fourth image information.

The Office Action alleges that col. 29, line 5-col. 31, line 5 of Baldwin teaches this feature. However, this section does not specifically teach or suggest at all any relations between the bits", the examiner points to the relational phrases "greater than"(>) as themselves pointing out what the relationship between the bits are.

19. In regards to the argument directed to the combination of Baldwin and Smith, has been answered in the rejections above.
20. In regards to the arguments directed to the dependent claims 2-6, 8-11, 13-17, 19-22, 24-28, and 30-33 rejections are not persuasive since the rejections to the independent claims stand.

Conclusion

21. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Frank M. Leiva whose telephone number is (571) 272-2460. The examiner can normally be reached on M-Th 8:30am - 5:pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert E. Pezzuto can be reached on (571) 272-6996. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

FML

04/14/2007



Robert E Pezzuto

Supervisory Patent Examiner

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